

ANALOG DEMODULATOR IN A LOW-IF RECEIVER

Abstract

An analog demodulator used in a low IF receiver to down-convert a pair of quadrature IF signals and to perform image-rejection operations. The analog demodulator includes at least one first calibration apparatus and/or at least one second calibration apparatus so that the analog demodulator can reduce DC offset that would cause LO leakage in the low IF receiver by using the first calibration apparatus and/or the second calibration apparatus. The analog demodulator further includes a filtering device connected to a LO generator for removing the 3rd and 5th order harmonic components.